

Fig. 1
(PRIOR ART)

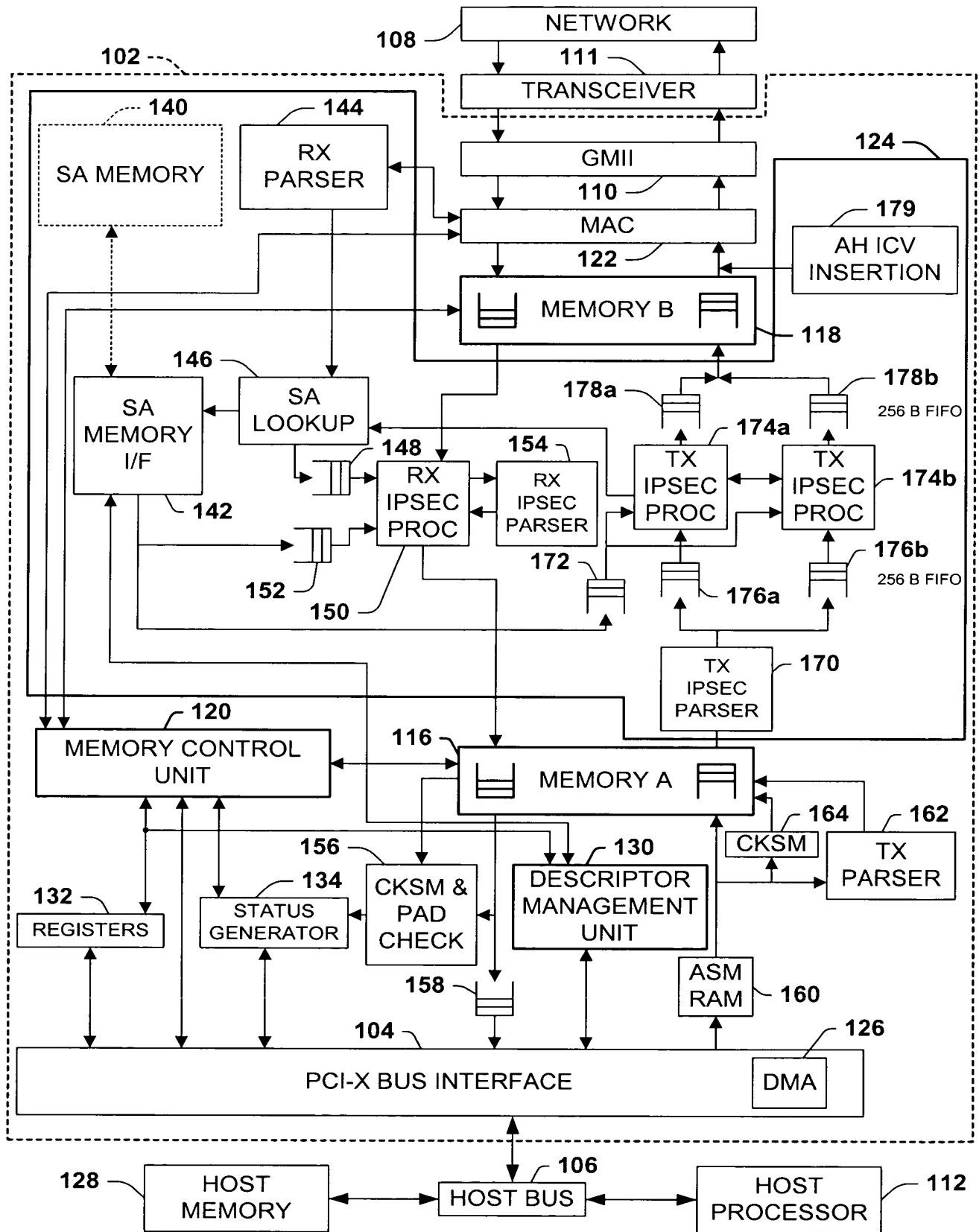


Fig. 2

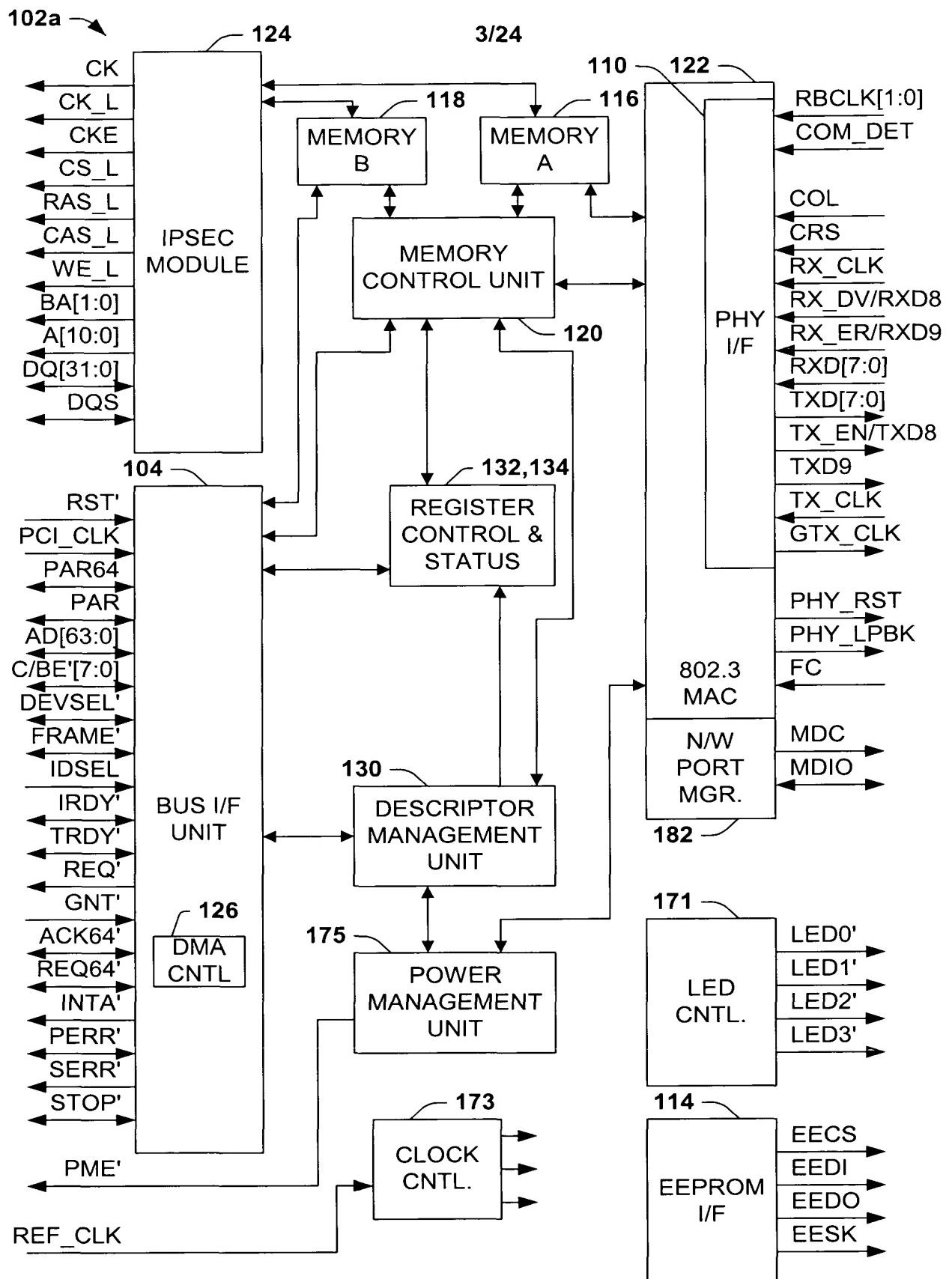
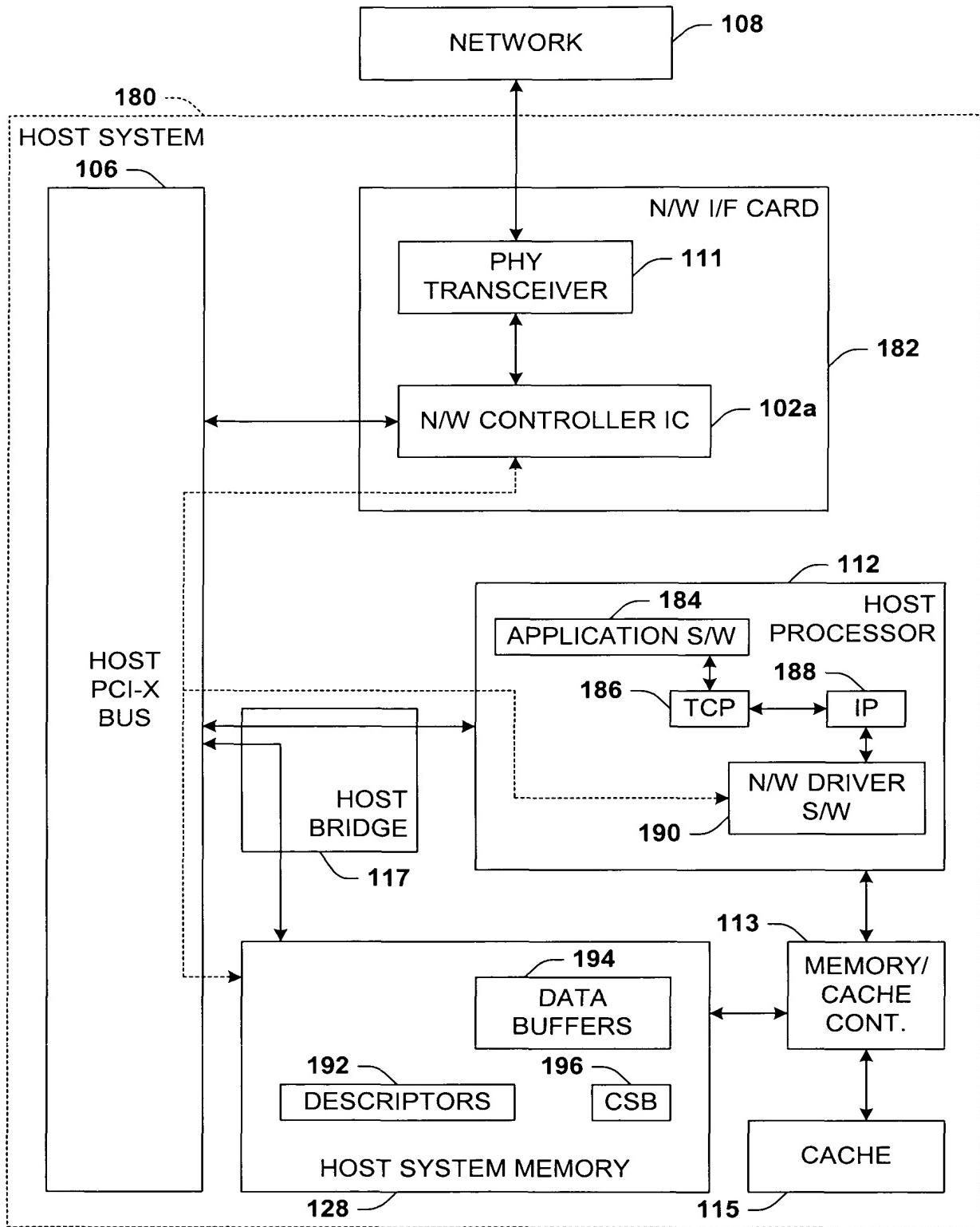
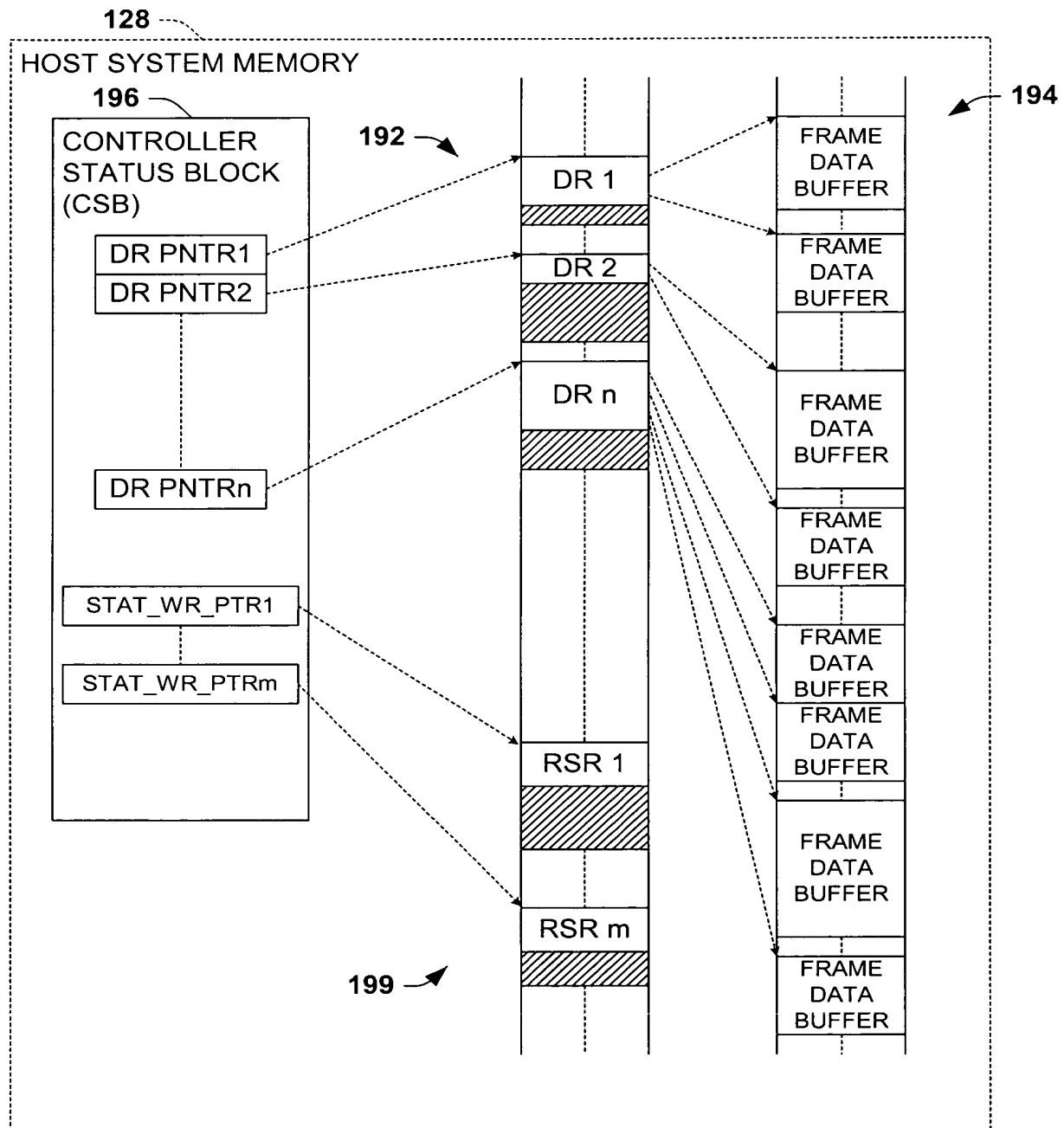
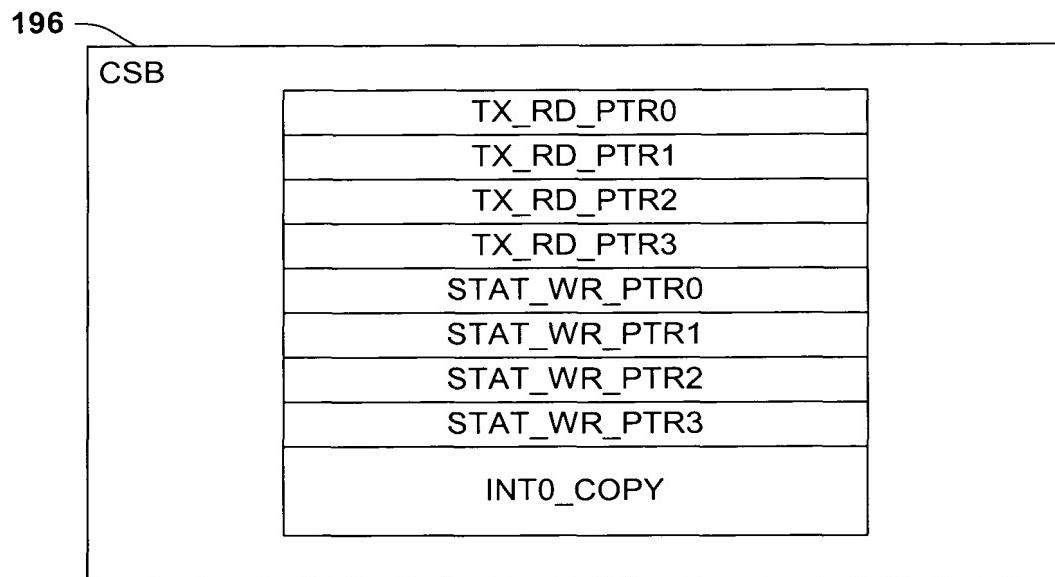
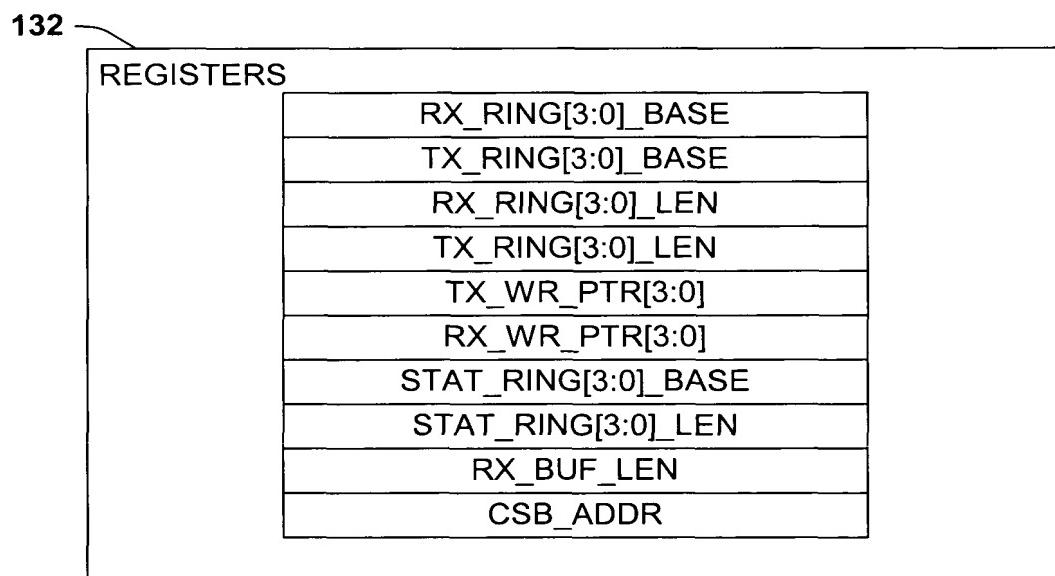
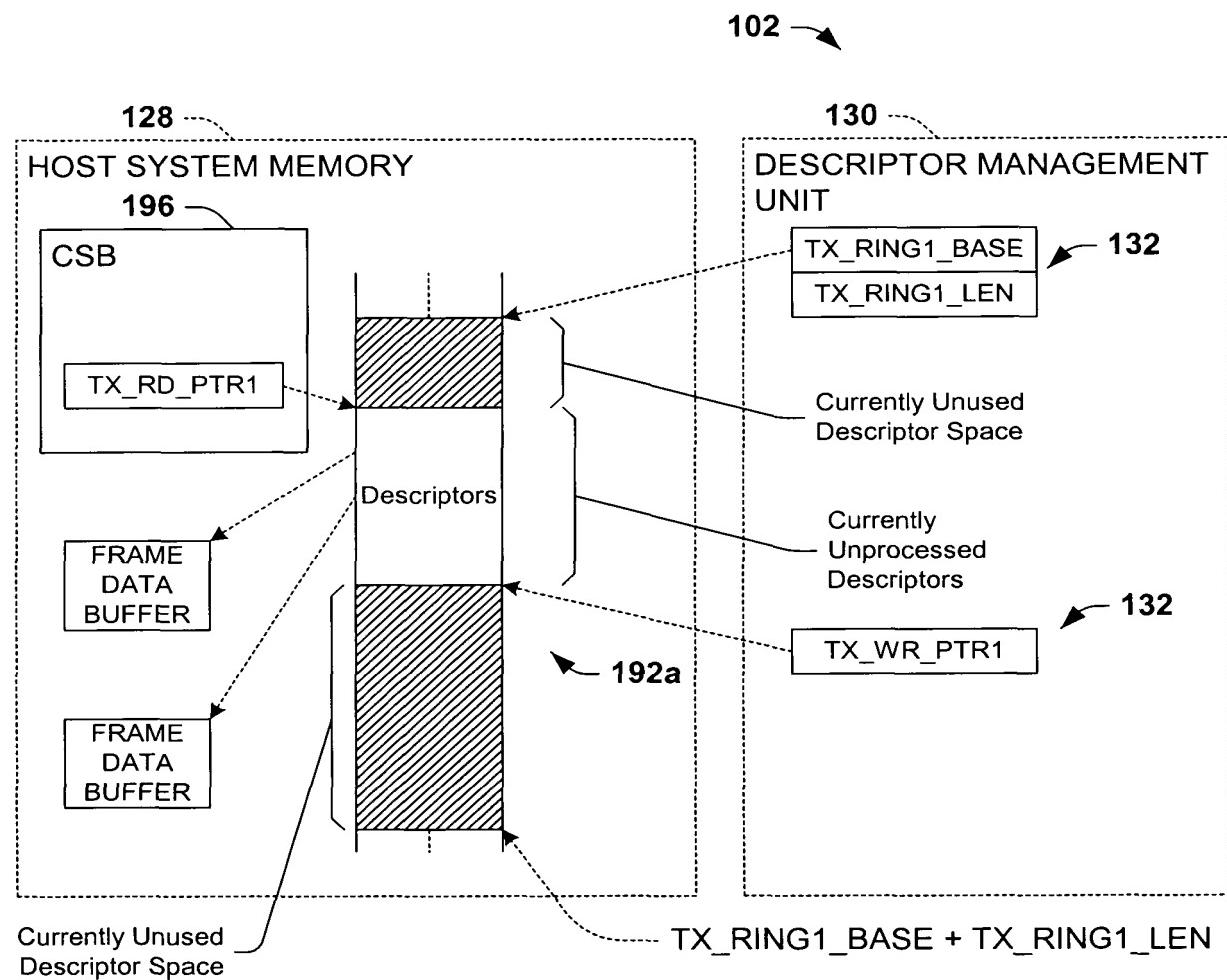


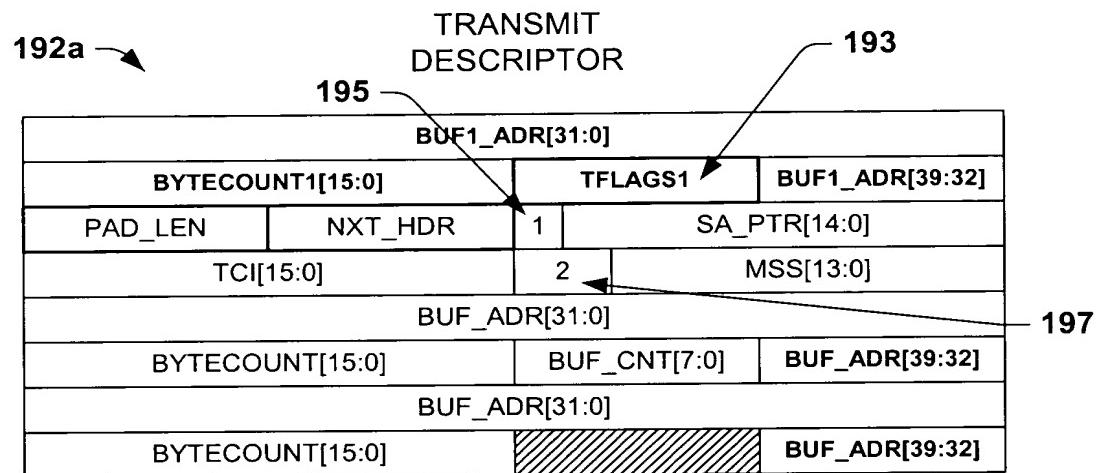
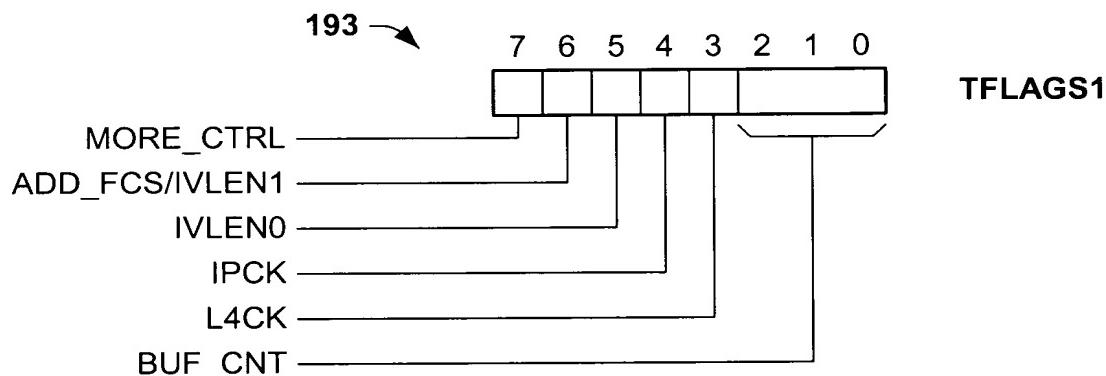
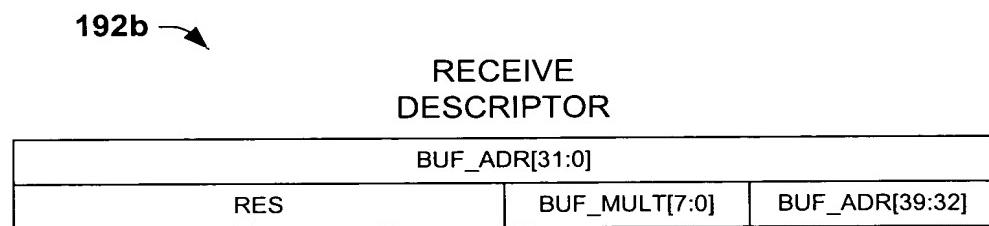
Fig. 3

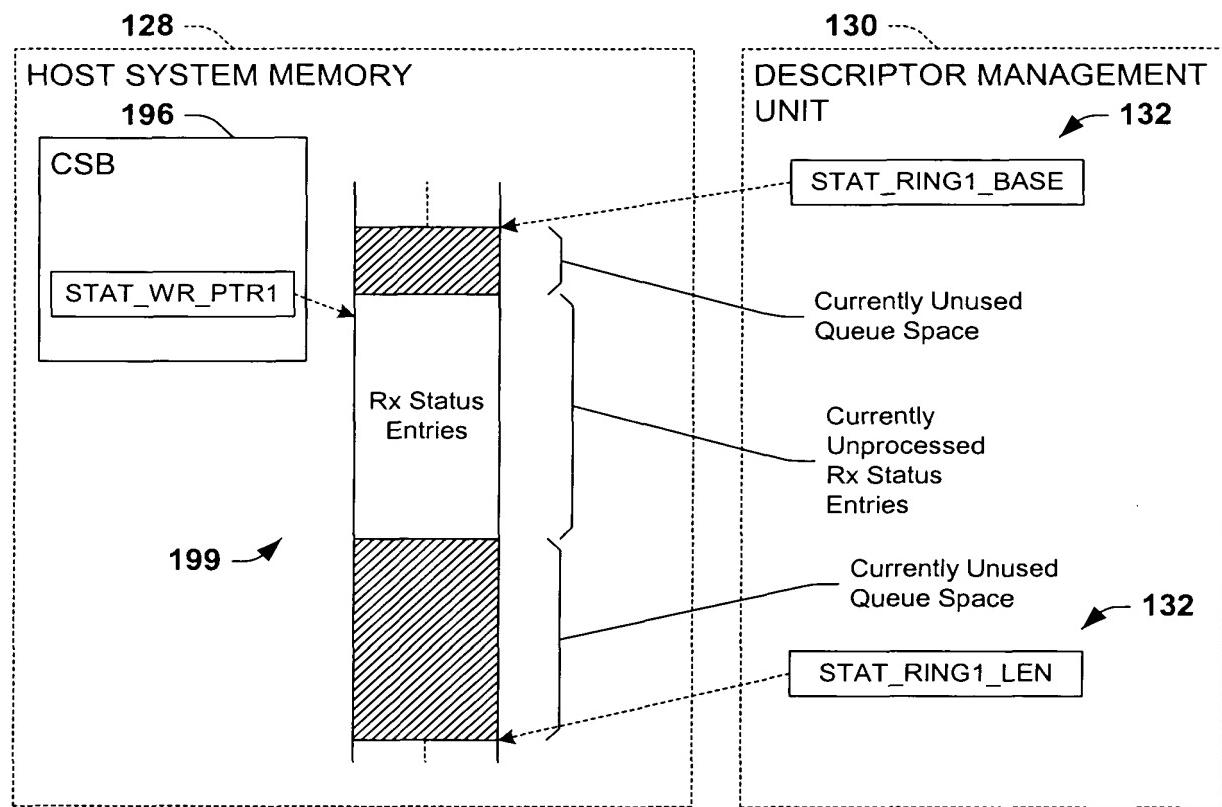
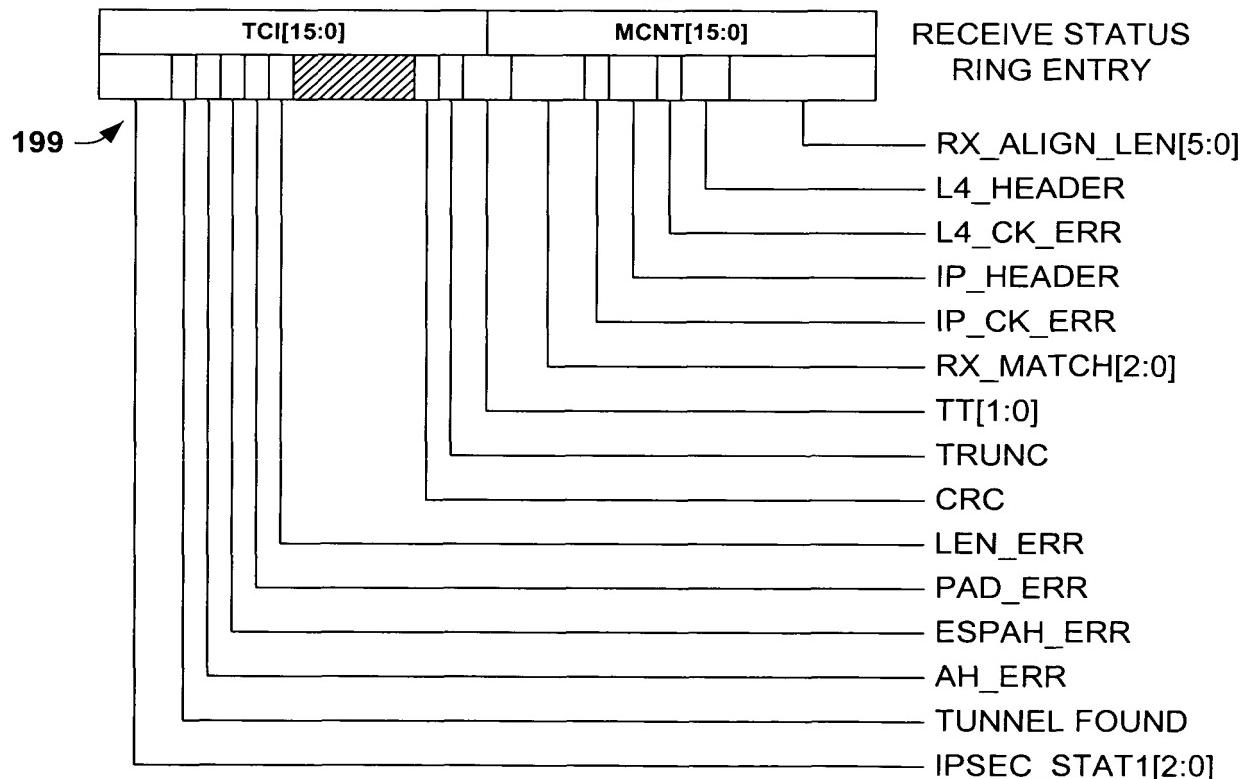
**Fig. 4**

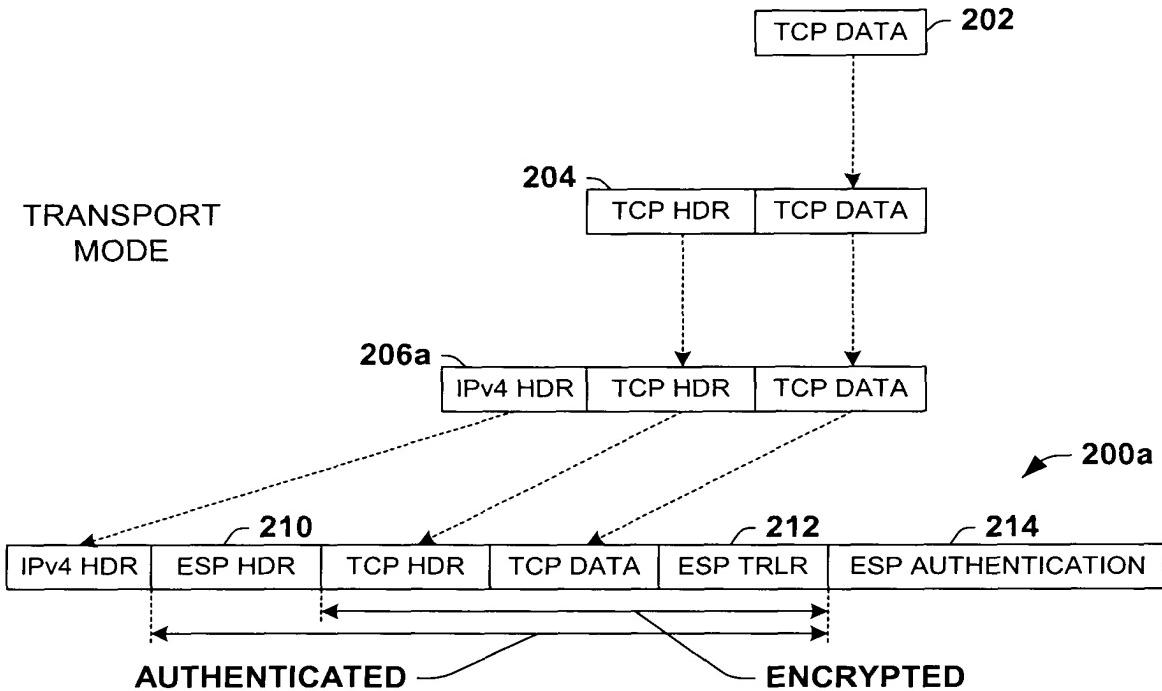
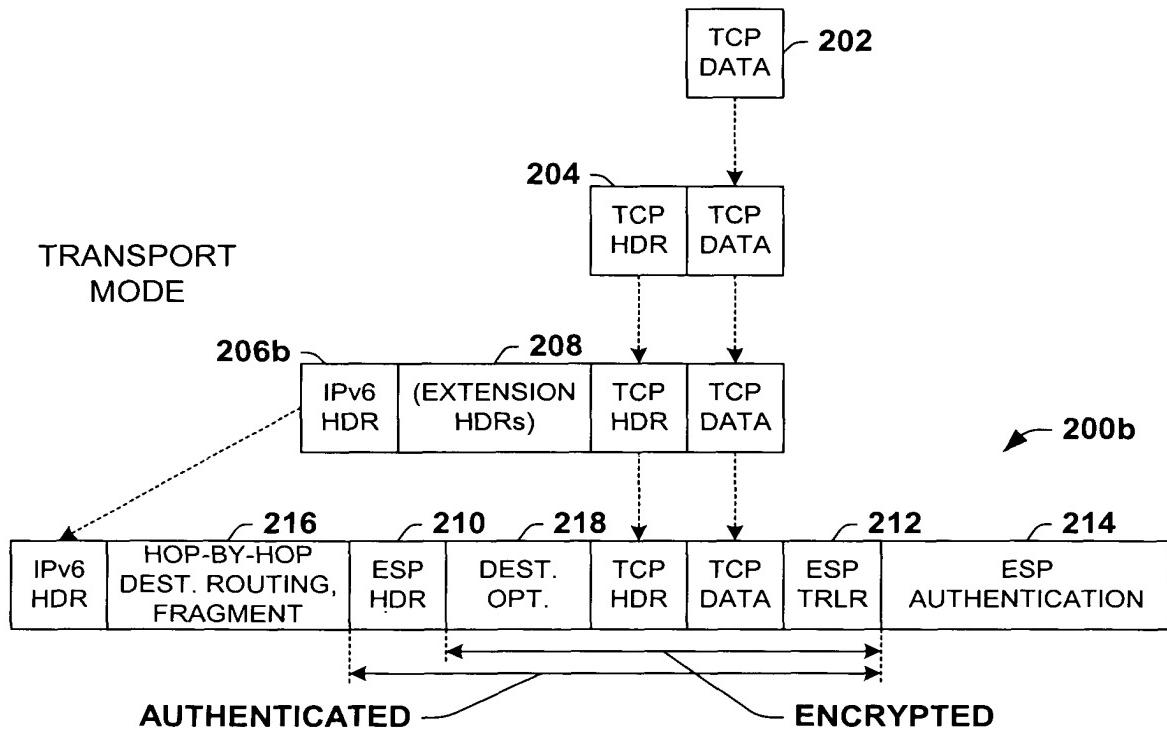
**Fig. 5A**

**Fig. 5B****Fig. 5C**

**Fig. 5D**

**Fig. 5E****Fig. 5F****Fig. 5G**

**Fig. 5H****Fig. 5I**

**Fig. 6A****Fig. 6B**

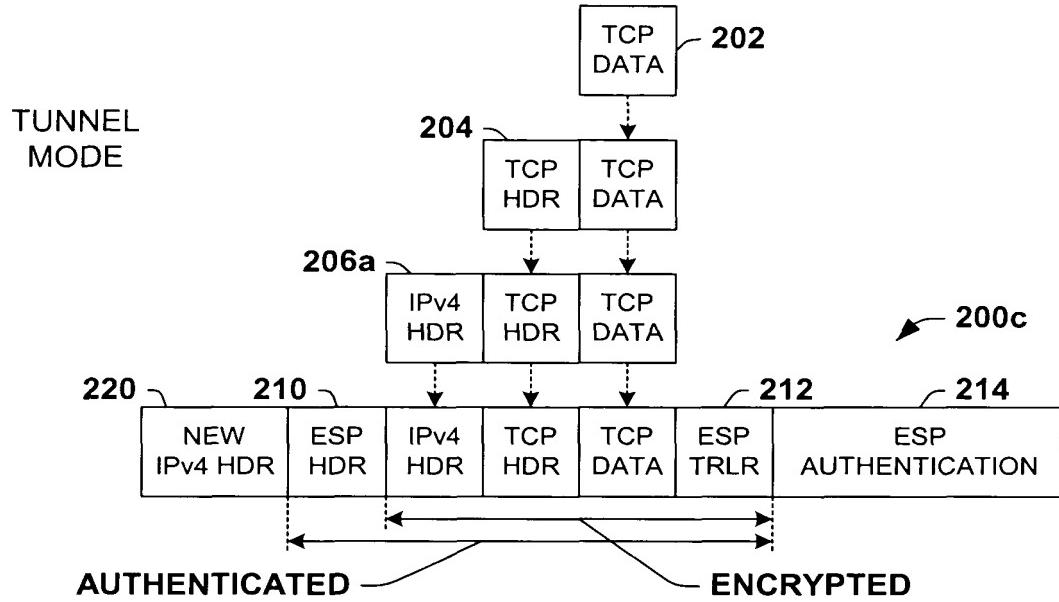


Fig. 6C

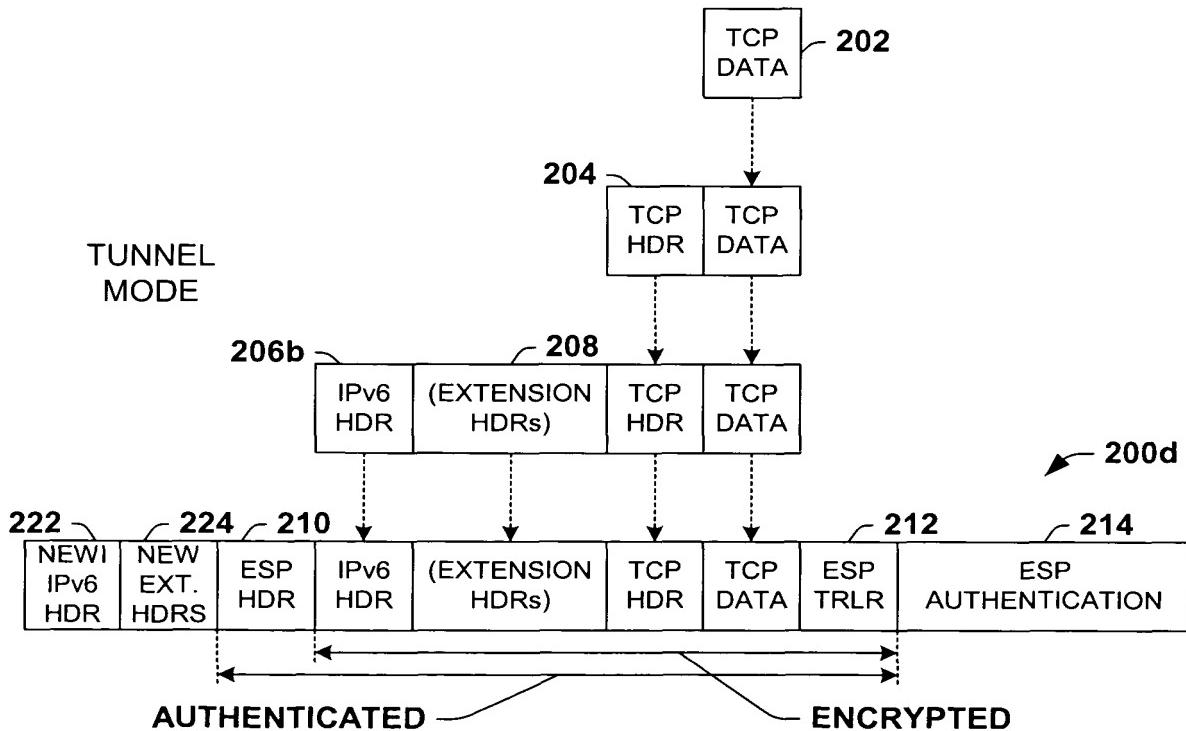


Fig. 6D

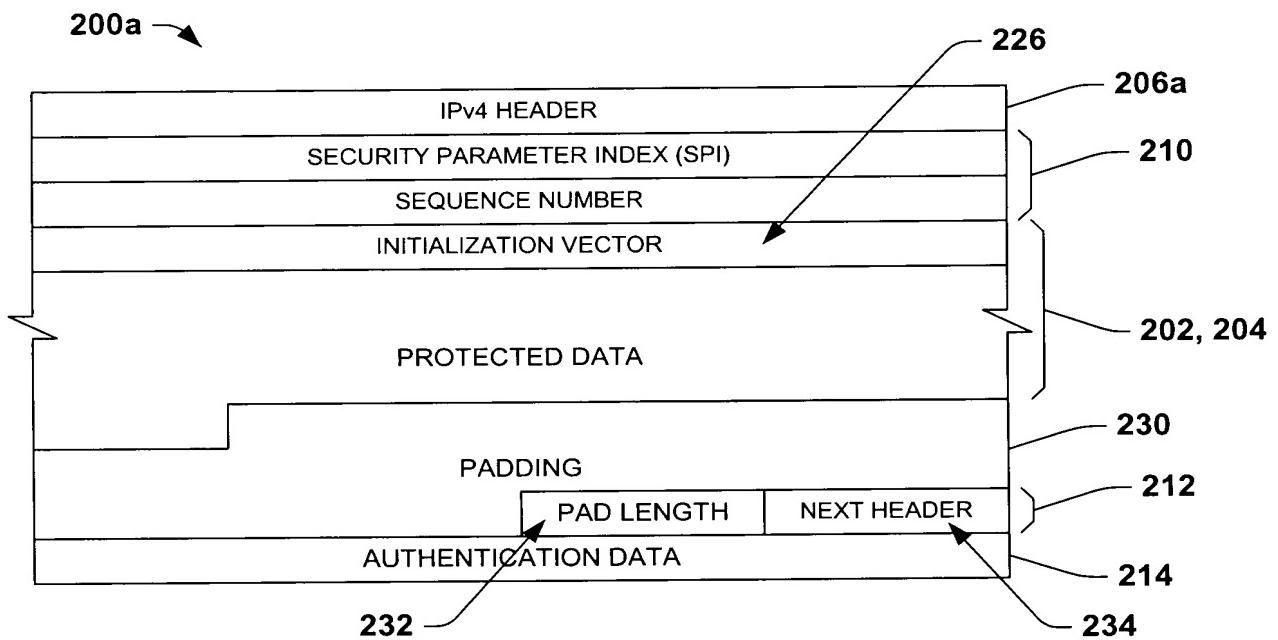


Fig. 6E

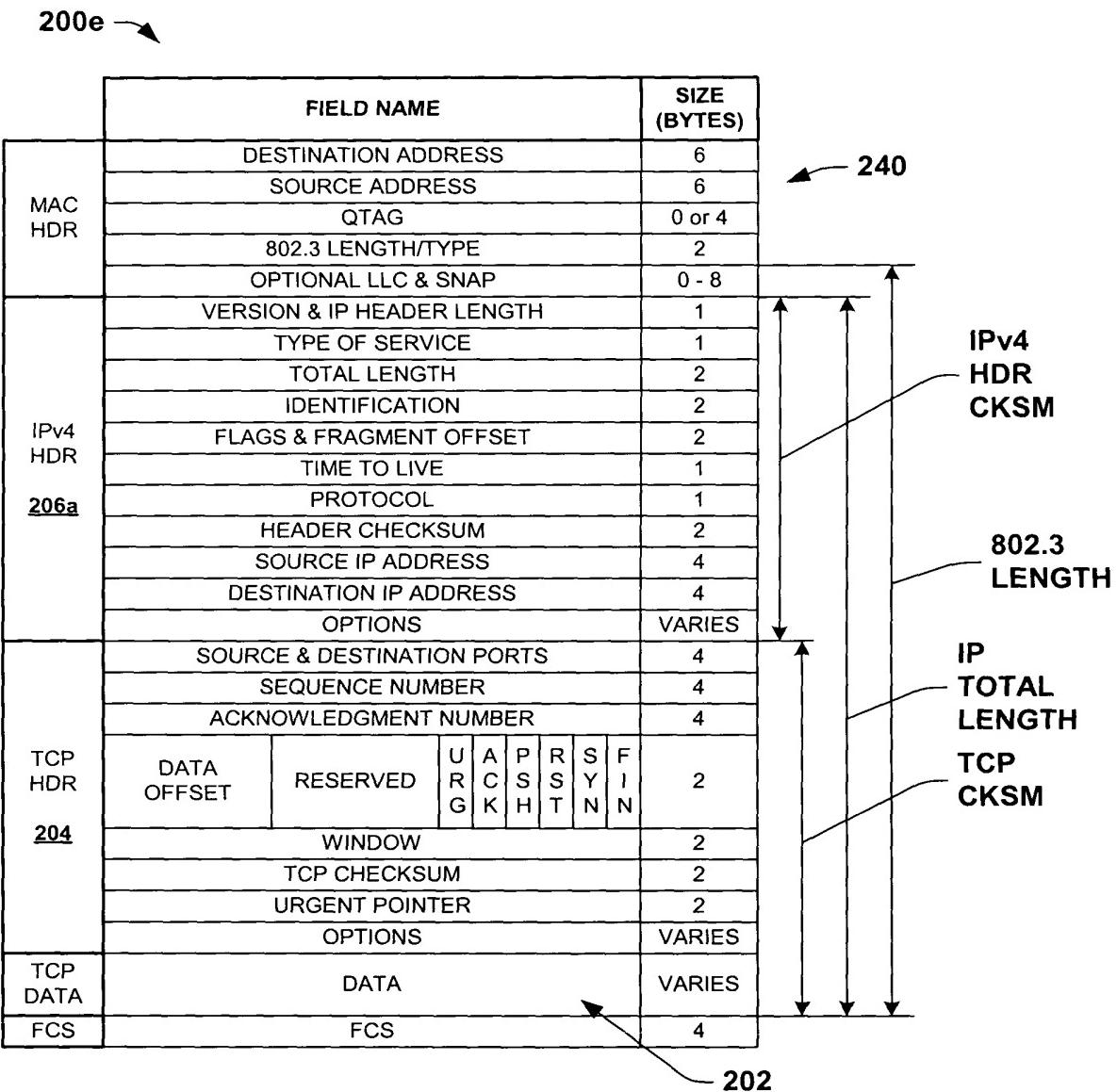


Fig. 7A

200f ↗

	FIELD NAME		SIZE (BYTES)
MAC HDR	DESTINATION ADDRESS		6
	SOURCE ADDRESS		6
	QTAG		0 - 4
	LENGTH/TYPE		2
	OPTIONAL LLC & SNAP		0 - 8
IPv6 HDR <u>206b</u>	VERSION, TRAFFIC CLASS, & FLOW LABEL		4
	PAYLOAD LENGTH		2
	NEXT HEADER		1
	HOP LIMIT		1
	SOURCE IP ADDRESS		16
	DESTINATION IP ADDRESS		16
	EXTENSION HEADERS		VARIABLES
TCP HDR <u>204</u>	SOURCE & DESTINATION PORTS		4
	SEQUENCE NUMBER		4
	ACKNOWLEDGMENT NUMBER		4
	DATA OFFSET	RESERVED	U A P R S F R C S S Y I G K H T N N
	WINDOW		2
	TCP CHECKSUM		2
	URGENT POINTER		2
	OPTIONS		VARIABLES
TCP DATA	DATA		VARIABLES
FCS	FCS		4

240 ↙

802.3 LENGTH ↘

PAYLOAD LENGTH ↘

TCP CKSM ↘

202 ↘

Fig. 7B

250 ↘

FIELD NAME	Created by Host	Modified by ESP	Encrypted	Covered by ESP Authentication	Added by Controller
Preamble					x
Start of Frame Delimiter					x
MAC Header	x				
IP Header	x				
ESP Header	x			x	
Payload Data	x	x	x	x	
Padding	x	x	x	x	
Pad Length	x	x	x	x	
Next Header	x	x	x	x	
Authentication Data	x	x			
Frame Check Sequence					x

Fig. 8A

252 ↘

FIELD NAME	Created by Host	Modified by AH Module	Covered by AH Authentication	Added by Controller
Preamble				x
Start of Frame Delimiter				x
MAC Header	x			
IP Header	x		x	
AH Header	x	x	x	
Other Headers	x		x	
Payload Data	x		x	
Frame Check Sequence				x

Fig. 8B

254 ↗

PHCKSM FOR IPv4	
32-BIT IP SOURCE ADDRESS	
32-BIT IP DESTINATION ADDRESS	
ZERO	PROTOCOL
TCP TOTAL LENGTH	

Fig. 8C

256 ↗

PHCKSM FOR IPv6	
128-BIT IP SOURCE ADDRESS	
128-BIT IP DESTINATION ADDRESS	
16-BIT TCP TOTAL LENGTH	
ZERO	PROTOCOL

Fig. 8D

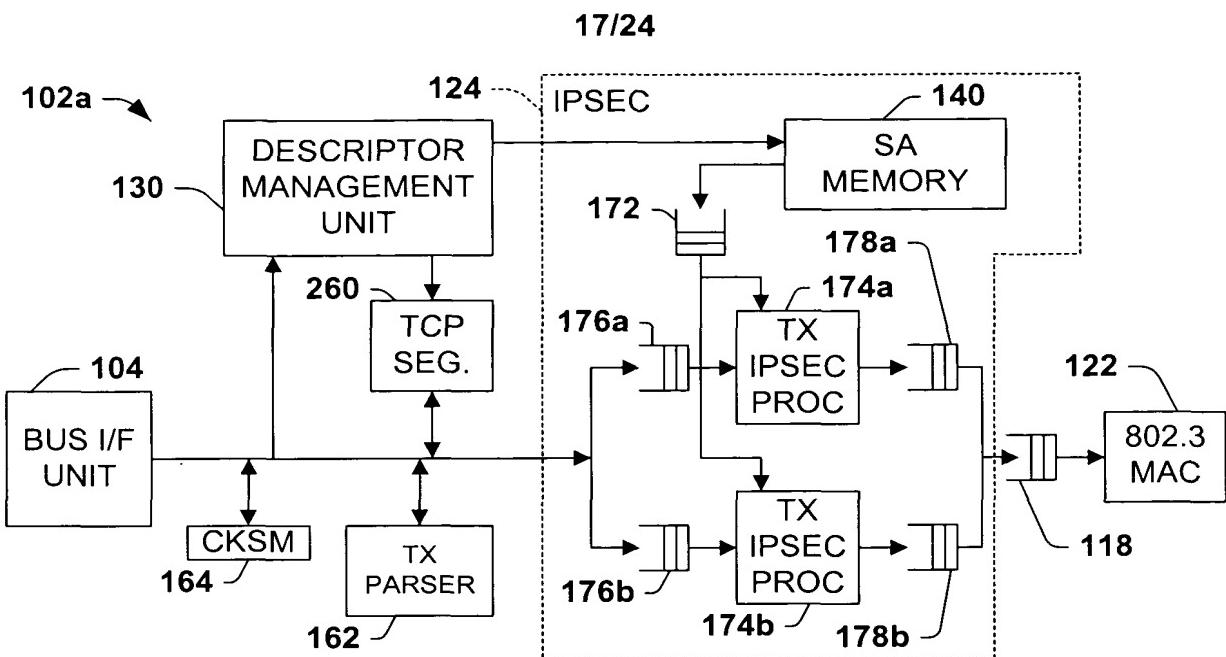


Fig. 9

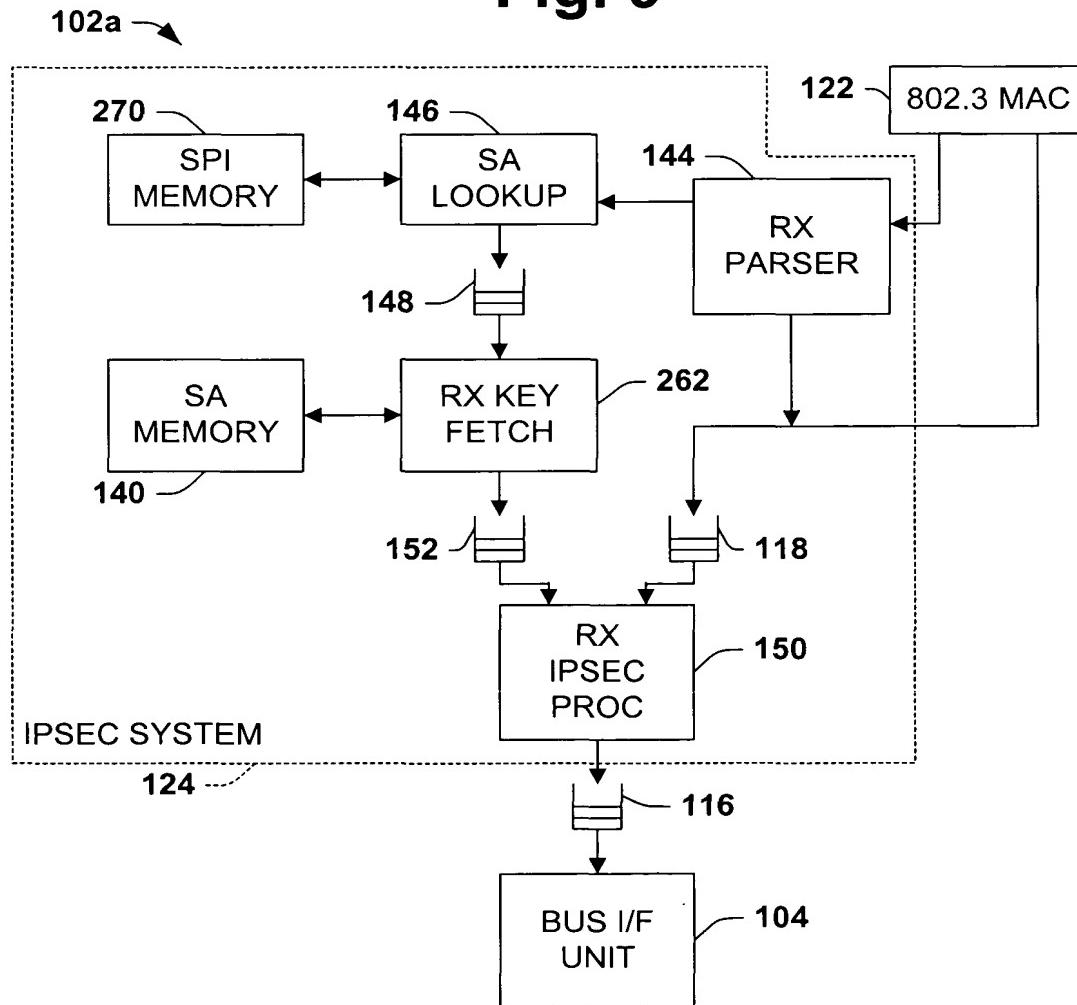


Fig. 10

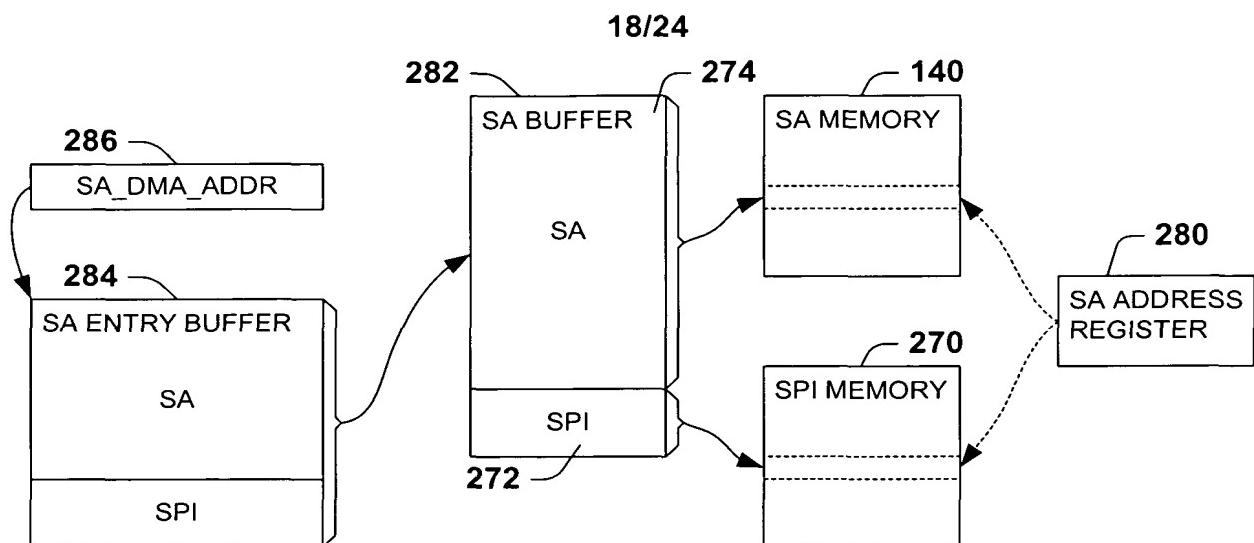


Fig. 11A

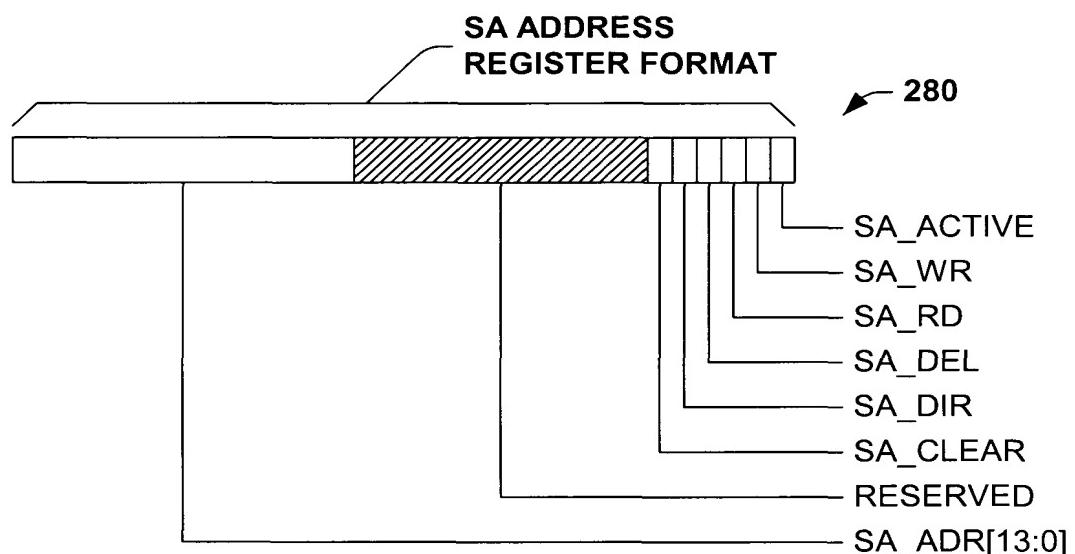


Fig. 11B

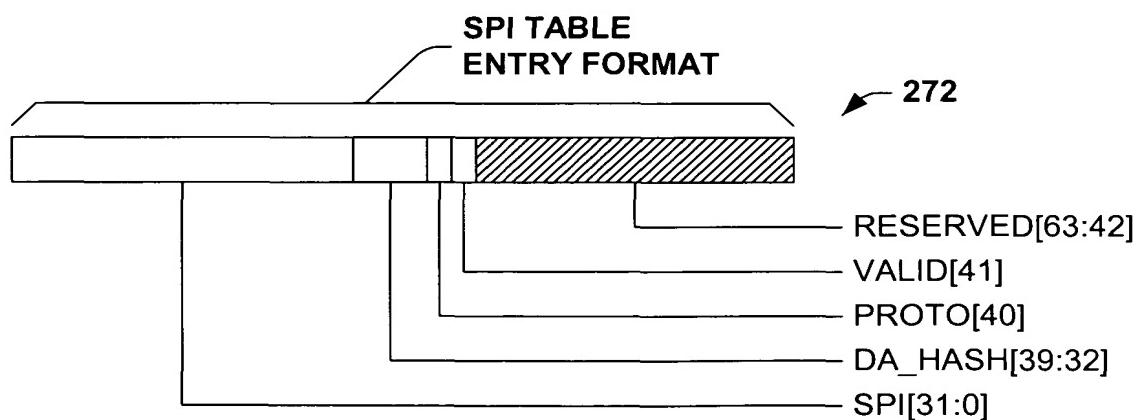
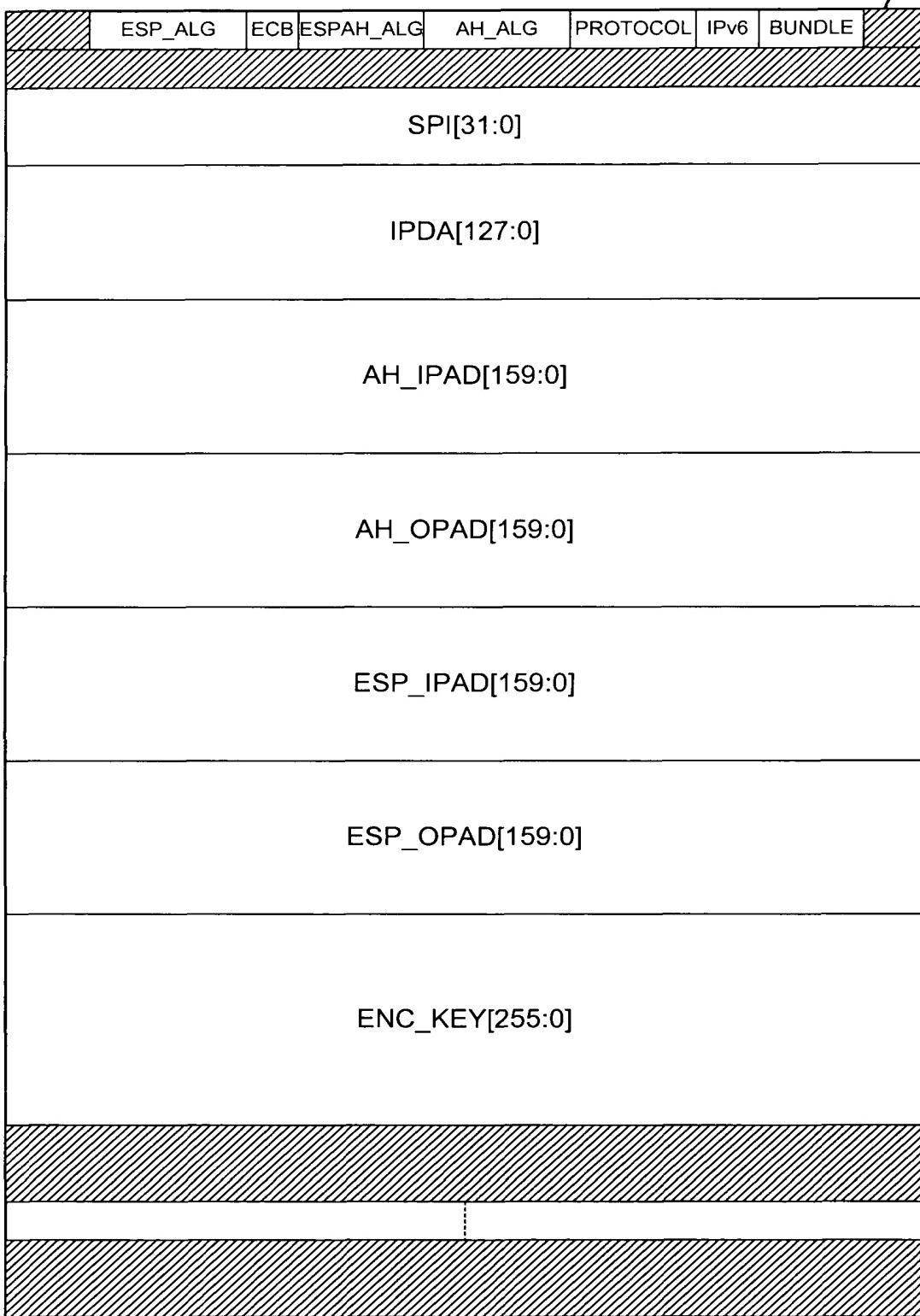


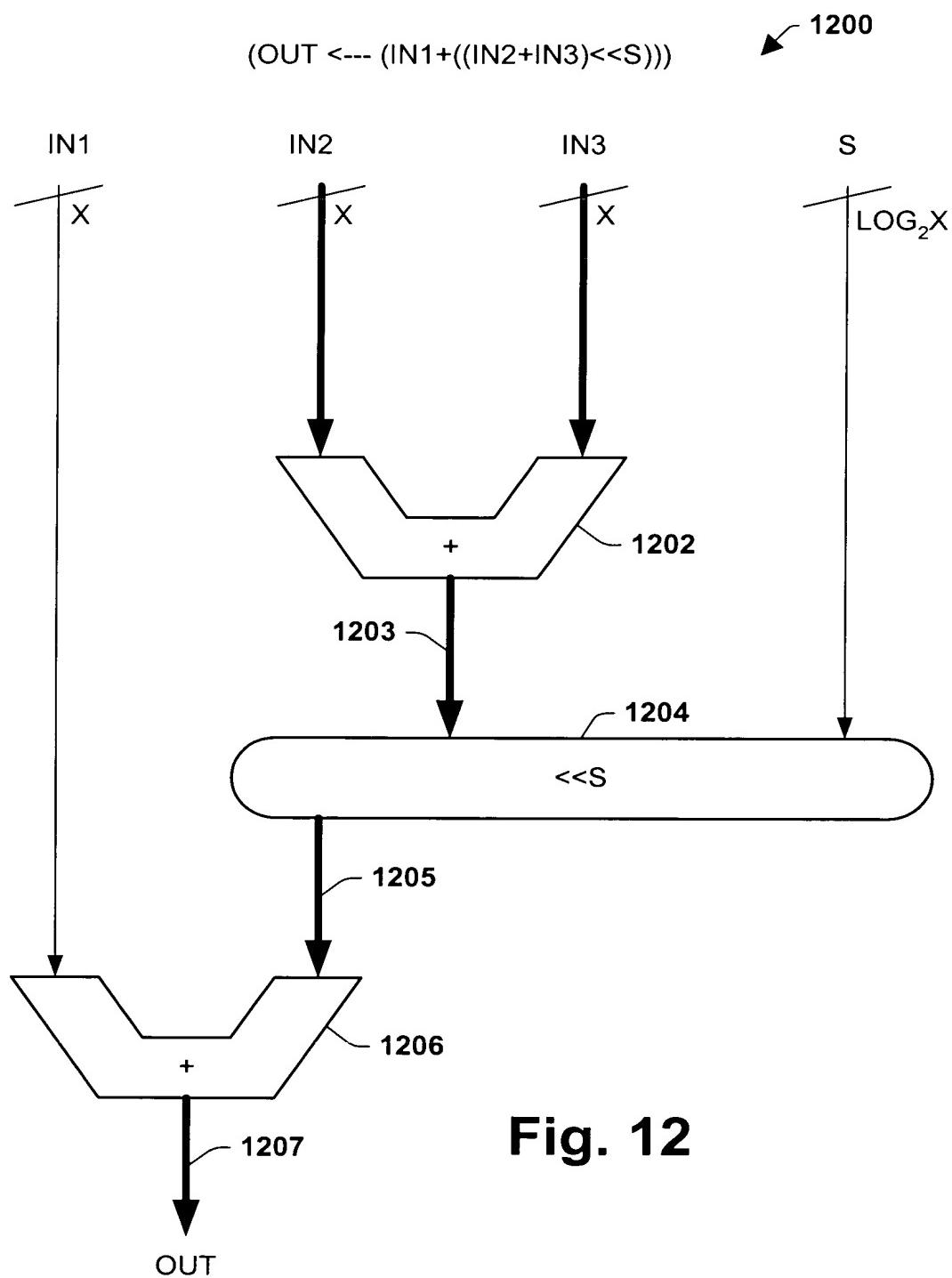
Fig. 11C

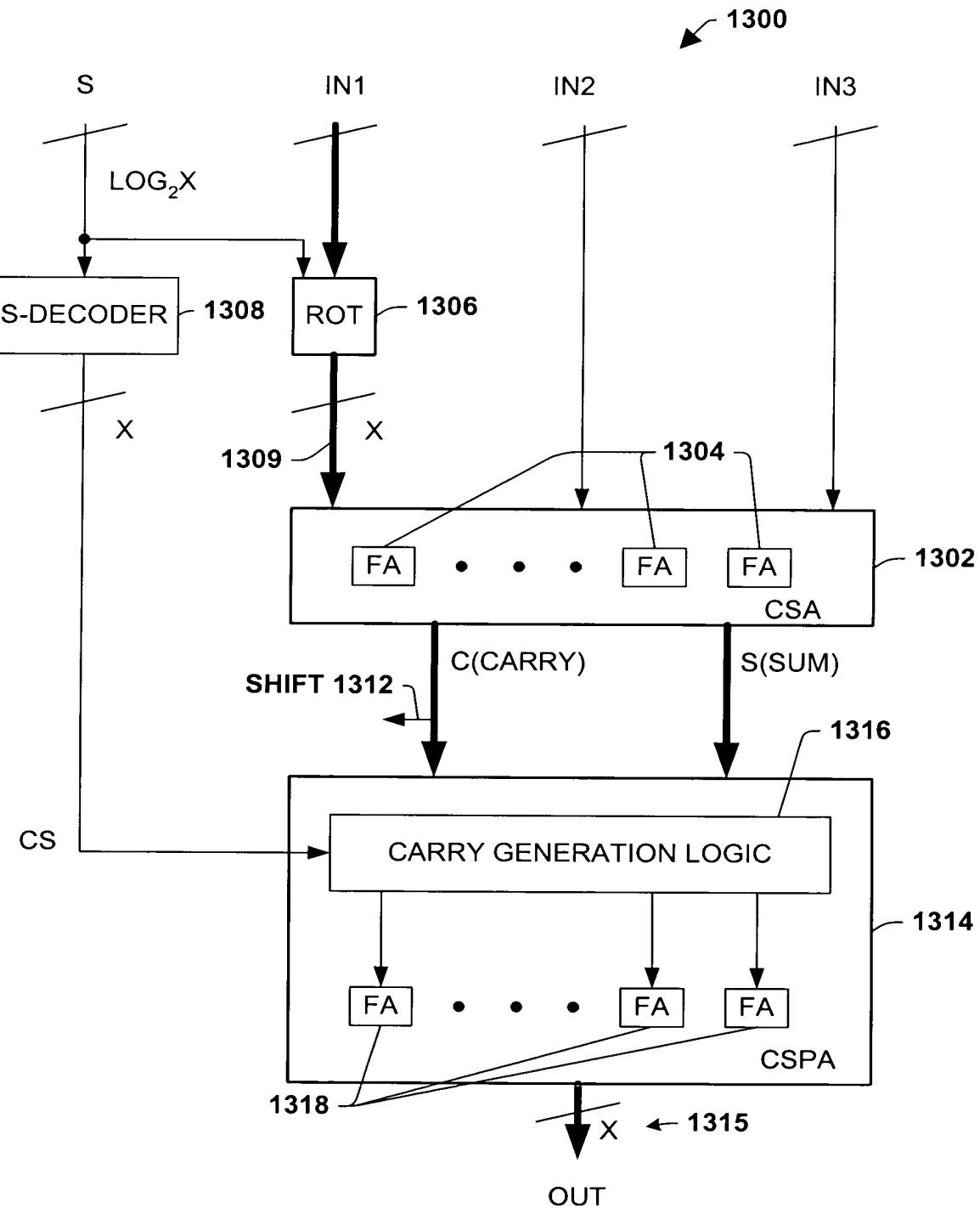
140 ↘

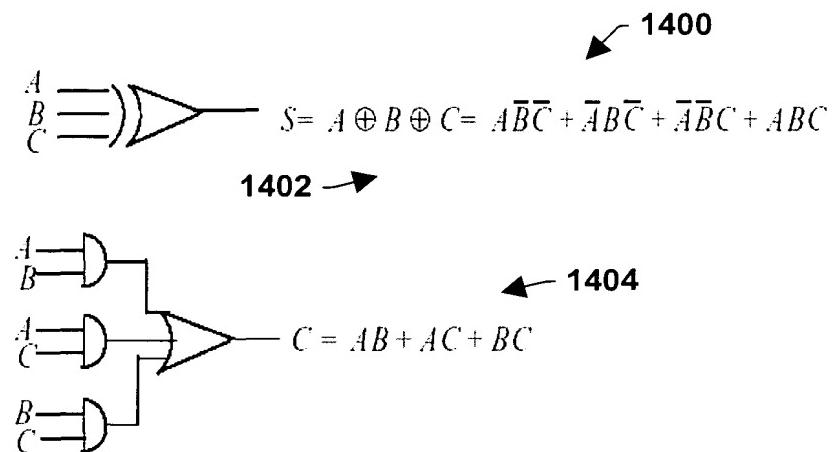
SA MEMORY ENTRY

274 ↗

**Fig. 11D**



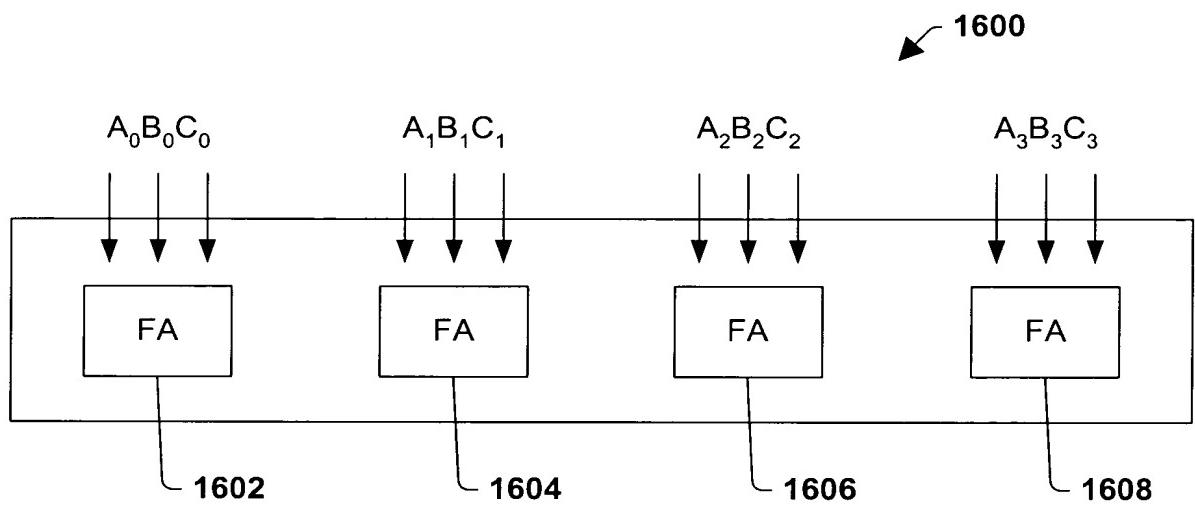
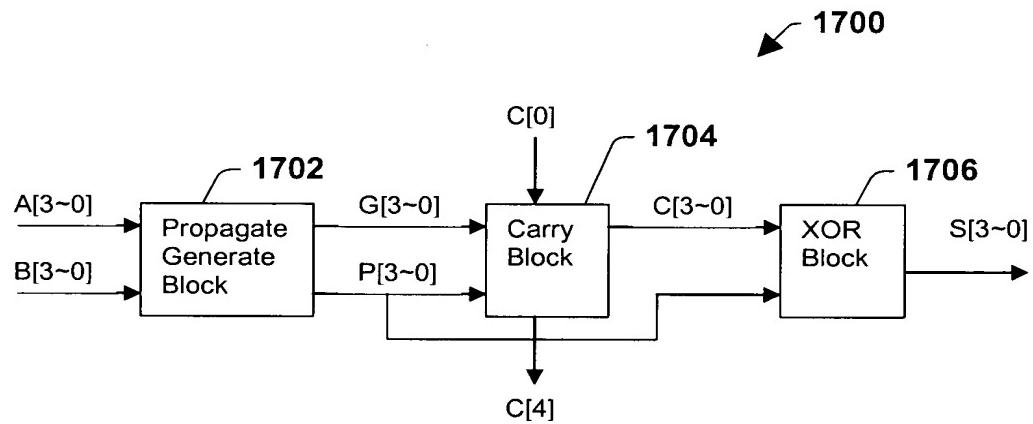
**Fig. 13**

**Fig. 14**

1500 ↙

INPUTS			OUTPUTS	
A	B	C_{IN}	C_{OUT}	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

Fig. 15

**Fig. 16****Fig. 17**

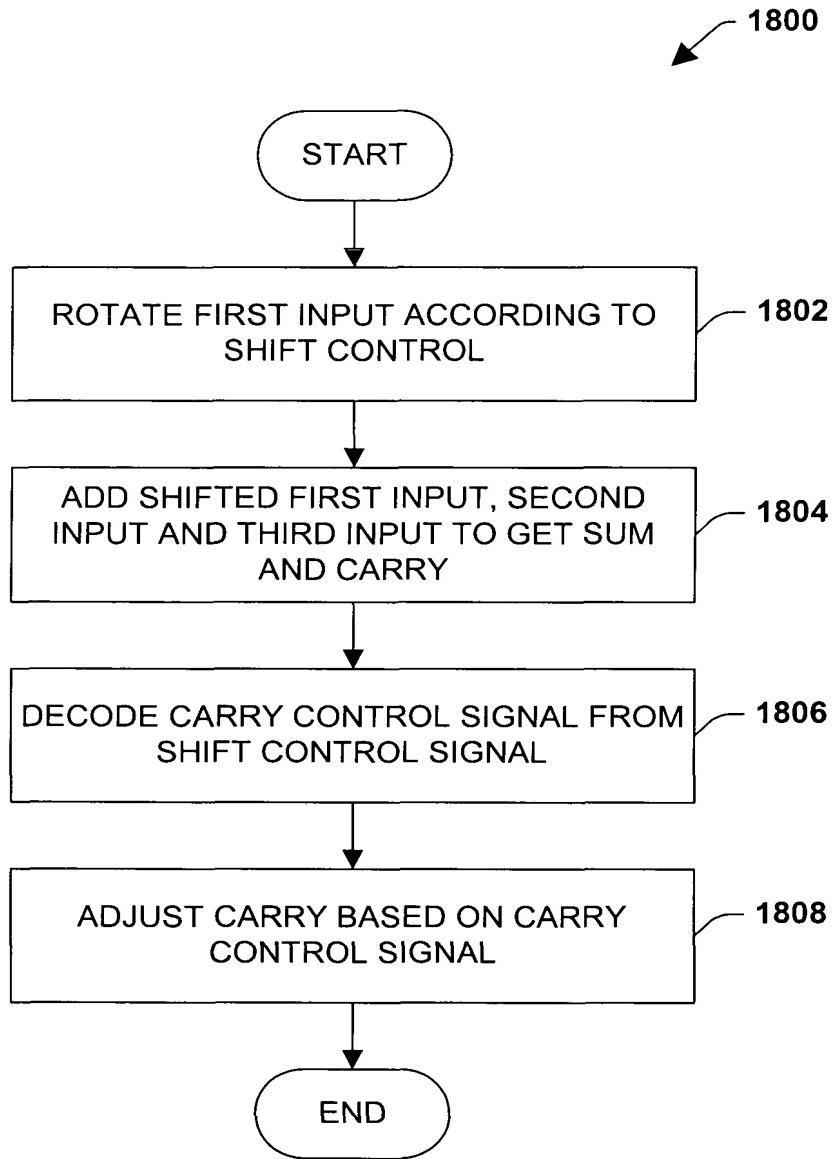


Fig. 18